

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:	) Confirmation No.: 3838
Hiroki Goko	)
Application No. 10/813,031	) Examiner: MEMULA, Suresh
Filed: March 31, 2004	) Group Art Unit: 2825
For: APPARATUS AND METHOD FOR DESIGNING	)
SEMICONDUCTOR INTEGRATED CIRCUIT	) Date: July 28, 2009

**REQUEST FOR RECONSIDERATION**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This is responsive to the final Office Action dated April 28, 2009 and to the examiner interview conducted on July 7, 2009. In view of the following remarks, reconsideration of the application is respectfully requested.